

Claims

1. A method of managing memory resources within an audio playback system, wherein the resources comprise memory blocks, the method comprising:

identifying a first and a second audio process, wherein the first and the second audio processes execute at different times;

allocating a first memory block to the first and the second processes;

associating a first audio frame with the first process;

buffering the first audio frame to the first memory block.

2. The method according to claim 1, further comprising buffering a second audio frame to the first memory block, wherein the second audio frame is associated with the second process.

3. The method according to claim 2, further comprising resetting a memory area of the first memory block to zero prior to receiving the second audio frame.

4. The method according to claim 1, further comprising identifying the first audio frame.

5. The method according to claim 1, further comprising processing the first audio frame.

6. The method according to claim 1, further comprising implementing program code to ensure that the first and the second audio processes run at different times.

7. The method according to claim 1, further comprising outputting the first audio frame.

8. The method according to claim 1, further comprising formatting the first audio frame.

9. The method according to claim 2, further comprising switching between the first and the second processes at a boundary of the first frame.

10 The method according to claim 9, further comprising muting volume parameters associated with the first memory block during the step of switching between the first and second processes.

11. An apparatus for managing memory within an audio playback system, comprising:

a memory area comprising a plurality of blocks;

program storage storing a program configured to allocate a first memory block from among the plurality of blocks to a first and a second audio process, wherein the first and the second processes execute at different times; and

a processor operable to execute the program, wherein the processor further buffers a first audio frame to the memory block according to a protocol of the program.

12. The apparatus of claim 11, wherein the program is configured to identify the first audio frame.

13. The apparatus of claim 11, wherein the program is configured to associate the first frame with the first audio process.

14. The apparatus of claim 11, wherein the program is configured to initiate processing of the first frame.

15. The apparatus of claim 11, wherein the program is configured to ensure that the first and the second audio processes run at different times.

16. The apparatus of claim 11, wherein the program is configured to initiate output of the first audio frame.

17. The apparatus of claim 11, wherein the program is configured to initiate formatting of the first audio frame.

18. The apparatus of claim 11, wherein the program is configured to initiate the buffering of a second audio frame to the first memory block, wherein the second audio frame is associated with the second process.

19. The apparatus of claim 18, wherein the program is configured to reset a memory area of the first memory block to zero.

20. The apparatus of claim 18, wherein the program is configured to allow switching between the first and the second processes only at a boundary of the first frame.

21. The apparatus of claim 20, wherein the program is configured to mute volume parameters associated with the first memory block while switching between the first and second processes.

22. A program product, comprising:

a program configured to manage memory resources within an audio playback system, wherein the resources comprise memory blocks, wherein the program is further configured to identify a first and a second audio process, wherein the first and second audio processes execute at different times; wherein the program allocates a first memory block to the first and second processes, associates a first audio frame with the first process, and buffers the first audio frame to the first memory block; and

a signal bearing medium bearing the program.

23. The program product of claim 22, wherein the signal bearing medium includes a recordable medium.

24. The program product of claim 22, wherein the signal bearing medium includes a transmission type medium.